FORM PTO-_9		Atty. Docket No.	Appln. No.
INFORMATION DISCLOSURE STATEMENT LIST OF DOCUMENTS CITED BY APPLICANT		XA-10241	10/518481
		Applicant	
		TONOMOURA, Motonobu et al.	
		Filing Date	Group Art Unit
		Herewith	

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						

FOREIGN PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AH	WO 91/20028	12/26/91	WIPO			
	AI	2001-56640	02/27/01	Japan			
	AJ	7-50595	02/21/95	Japan			
	AK						
	AL						
	AM						
	AN						

OTHER (including author, title, date, pertinent pages, etc.)

	AO	Ramabadran, T. V. et al., "A Tutorial on CRC Computations," IEEE Micro, Vol. 8, No. 4, August 1988, pp. 62-75.
	AP	Moon, S. et al. "Fast VLSI Arithmetic Algorithms for High-Security Elliptic Curve Cryptographic Applications," IEEE Transaction Consumer Electronics, Vol. 47, No. 3, August 2001, pp. 700-708.
	AQ	Mastrovito, E., "VLSI Designs for Multiplication over Finite Fields $GF(2^m)$," Proc. Sixth Int'l Conf. "Applied Alegbra, Algebraic Algorithms, and Error-Connecting Codes (AAECC-6)," July 1988, pp. 297-309.
	AR	Shiba, K. et al., "$GF(2^m)$ Ensan oyobi Seisu Ensan o Shori Kano na Hybrid Coprocessor no Teian," 1999nen Ango to Joho Security Symposium Yokoshu, Volume II of II, January 26, 1999, pp. 819-824.

Examiner	Date Considered	
/Shahrouz Yousefi/	02/22/2008	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /S.Y./